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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/714,863	11/18/2003	Bonkee Kim	GK-US015199-B	3711
22919	7590	07/29/2004	EXAMINER	
SHINJYU GLOBAL IP COUNSELORS, LLP 1233 20TH STREET, NW, SUITE 700 WASHINGTON, DC 20036-2680				MOTTOLA, STEVEN J
ART UNIT		PAPER NUMBER		
				2817

DATE MAILED: 07/29/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/714,863 Examiner Steven J. Mottola	KIM ET AL. Art Unit 2817	JK

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 18 November 2003.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 12-14 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 12 and 13 is/are rejected.
 7) Claim(s) 14 is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. 10/051,290.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>111803</u> . | 6) <input type="checkbox"/> Other: _____ |

The disclosure is objected to because of the following informalities: the specification should be updated at page 2 to reflect the updated status of the parent application.

Appropriate correction is required.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 12 is rejected under 35 U.S.C. 102(b) as being anticipated by Kamali et al.

Note initially that the ‘bar’ under 35 U.S.C. 102(b) could be removed by a certified translation of the priority document but that the reference would still be applicable under 35 U.S.C. 102(e). Refer to fig. 3 of Kamali et al. which shows a diagram of an Anadigics ACA0861 type amplifier (col. 2, lines 52-55) including an upper stage having transistors Q1,Q3 which may be read as the first unit having first and second active devices claimed and a lower stage having transistors Q2,Q4 which may be read as the second unit having third and fourth active devices claimed. Resistor R1 is connected to the gates of Q1 & Q2 (read as the first and third active devices) as well as the input. Supply Vdd is shown connected via chokes the overall circuit without showing the exact supply interconnection to the transistors. However a more detailed diagram of an Anadigics ACA0861 series amplifier would show that the connection is to the

outputs (here sources) of transistors Q3,Q4, read as the second and fourth active devices claimed. See the evidence of the Anadigics application note cited below but not relied upon. Thus the supply VDD, chokes L and resistor R1 may be read as the biasing means claimed; that they bias both stages or units to be active meets the limitation of the last phrase of the claim as there is no limitation that one be biased off. Regarding claim 13, the balun at the input to any amplifier pair (figs. 4 or 5) will ensure that the signals are out of phase between the stages or units.

Claim 14 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
It is not apparent from Kamali et al. that one stage is NMOS and the other PMOS.

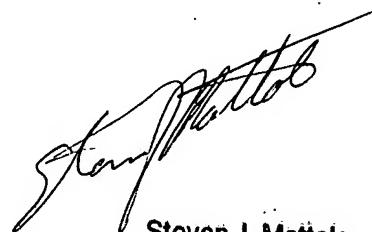
The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Note the Anadigics application note for the ACA0861 series amplifiers, in particular figs. 3,4 and 6 that show the supply connected to the output lines via chokes. The patent issued from the parent application to the present application is cited as Kim et al.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven J. Mottola whose telephone number is 571-272-1766. The examiner can normally be reached on M-Th from 8 to 5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert J. Pascal, can be reached on 571-272-1769. The

fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Steven J. Mottola
Primary Examiner